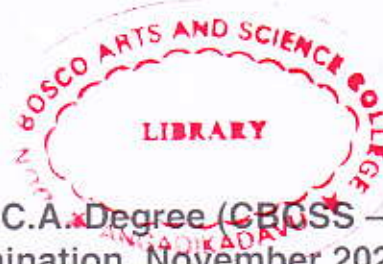




K21U 2142

Reg. No. : .....

Name : .....



III Semester B.C.A. Degree (CBSS – Sup./Imp.)  
Examination, November 2021  
(2015-'18 Admissions)  
Core Course  
3B06BCA : COMPUTER ORGANIZATION

Time : 3 Hours

Max. Marks : 40

SECTION – A

1. One-word answer. (8×0.5=4)
- a) The \_\_\_\_\_ holds the instruction that is currently being executed.
  - b) In \_\_\_\_\_ mode the operand is given explicitly in the instruction.
  - c) \_\_\_\_\_ is the storage location for the temporary storage of information during the process of writing to or reading from main memory.
  - d) In 8085 microprocessor number of machine cycles required for RET instruction is \_\_\_\_\_.
  - e) The DMA controller is directly connected to the \_\_\_\_\_ to provide faster transfer of data.
  - f) Floating point representations is used to store \_\_\_\_\_.
  - g) Cache memory acts between \_\_\_\_\_ and \_\_\_\_\_.
  - h) \_\_\_\_\_ is a method of accessing computer memory or bus without interfering with the CPU.

SECTION – B

Write short notes on **any seven** of the following questions. (7×2=14)

- 2. Explain different functional units of a computer.
- 3. What is multiprogramming ?

P.T.O.



4. Define register transfer language.
5. What is a register ?
6. What is priority interrupt ?
7. Define cache memory.
8. Define control word.
9. Define write-through method.
10. Define polling.
11. Write a note on reverse polish notation.

SECTION – C

Answer **any four** of the following questions.

(4×3=12)

12. Explain floating point representation.
13. Explain RISC in detail.
14. Explain direct mapping in detail.
15. Write a short note on crossbar switch.
16. Write a note on DMA controller.
17. Distinguish between full duplex and half duplex transmission.

SECTION – D

Write an essay on **any two** of the following questions.

(2×5=10)

18. Explain basic operational concept of a computer with the help of diagram.
  19. Explain different asynchronous data transfer method in detail.
  20. Write a note on addressing modes and its various types.
  21. Explain different modes of data transfer to and from peripherals.
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