K23U 1076

Reg. No. : ....

Name : ....

IV Semester B.C.A. Degree (CBCSS-OBE-Regular/Supplementary/ Improvement) Examination, April 2023
(2019 Admission Onwards)
Core Course
4B09BCA : COMPUTER ORGANIZATION

Time : 3 Hours

Max. Marks : 40

PART – A

(Short Answer)

Answer **all** questions.

(6×1=6)

1. What is the use of the Instruction Register (IR) ?

2. Describe two address instructions.

3. What is Micro Programmed Control ?

4. Describe the property of the locality of reference.

5. Describe write-through and write-back caching.

6. Describe the Page Replacement Algorithm.

PART – B

(Short Essay)

Answer **any six** questions.

(6×2=12)

7. Define Memory Reference Instructions and give example.

8. Write notes on the central processing unit.

9. Write short notes on Peripheral Devices.

P.T.O.

10. Describe Asynchronous Data Transfer.

11. Explain the significance of memory page table in a virtual memory system.

12. What is the significance of memory hierarchy design in increasing system performance ?

13. What are the different parallel processing mechanisms in a uniprocessor system ?

14. Describe the pipeline stall.

PART – C

(Essay)

Answer **any four** questions.   (4×3=12)

15. Write short notes on Bus organization.

16. Explain interrupts.

17. Explain general register organization.

18. Describe instruction formats.

19. What is DMA ?

20. Explain instruction pipelining.

PART – D

(Long Essay)

Answer **any two** questions.   (2×5=10)

21. Explain different addressing modes.

22. Explain the Input Output processor and different modes of data transfer.

23. Explain virtual memory management.

24. Explain the characteristics and interconnection structure of multiprocessor systems.

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