



K15U 0096

Reg. No.:

Name :

III Semester B.C.A. Degree (CCSS – Supple./Improve.)

Examination, November 2015

B.C.A. – Core Course

3B07 BCA : COMPUTER ORGANIZATION

(2013 and Earlier Admissions)

Time : 3 Hours

Max. Weightage : 21

SECTION – A

Answer **all** questions. Weightage for a bunch of **four** questions is **1**.

1. Interrupts initiated by an instruction is called _____
2. _____ represents a signed fixed point number.
3. Simultaneous transfer of all bits from the source register to the destination register is called _____
4. MBR stands for _____ **(W = 1)**
5. In the _____ mode the operand is specified in the instruction itself.
6. A common bus attached to all peripheral interfaces is _____
7. The recording surface of memory devices is divided into
 - a) Pages
 - b) Blocks
 - c) Sectors
 - d) None of these
8. PSW stands for _____ **(W = 1)**

P.T.O.



SECTION – B

Answer **any 5** questions. Weightage - **1 each**.

9. What is EPROM ?
10. What is an instruction code ?
11. What is a Register Transfer Language ?
12. What is an external interrupt ?
13. What is associative memory ?
14. Explain DMA.
15. What is RISC ?
16. What is a multiprocessor computer ?

(5×1=5)

SECTION – C

Answer **any 5** questions. Weightage **2 each**.

17. What is a fetch cycle ?
18. Explain the various cache memory schemes.
19. Compare Register Addressing and Register Indirect Addressing.
20. Explain instruction pipelining
21. What is virtual memory ?
22. What is the use of PSW ?
23. What are maskable and non-maskable interrupts ?
24. Explain floating point representation of numbers.

(5×2=10)

SECTION – D

Answer **any one** question. Weightage : **4**.

25. Explain the instruction cycle.
26. Explain about the various types of computers.

(1×4=4)
